

ITC5730

dynamic parametric test system
for discrete semiconductors



MAINFRAME SPECIFICATIONS

Basic System Capabilities	<i>Maximum Test Voltage</i>	600Vdc@150A	
	<i>Minimum Timing Measurement</i>	1ns	
Physical Dimensions	<i>Height</i> = 65 inches	<i>Width</i> = 24 inches	
	<i>Depth</i> = 29 inches	<i>Weight</i> = 450 pounds	
ITC5721 Switching Time Specifications	<i>Pulse Width</i>	0.1 μ s to 10 μ s in 0.1 μ s Steps	
	<i>Gate Drive Voltage</i>	7 to 13 V @ \pm 0.1 volt Resolution	
MIL STD 750 Method 3472	<i>Gate/Current</i>	Maximum Gate Current = 1.0 A	
	<i>Drain/Current</i>	Maximum Pulsed Current = 100 A	
	<i>Duty Cycle</i>	Less Than 0.1 %	
	<i>Supply Voltage (V_{DD})</i>	20V to 600V in 2.5V Steps	
ITC5722 Trr/Qrr Specifications	<i>Forward Current (IF)</i>	ITC5722B = 1.0 - 50 A @ 0.2 A Resolution 50.5 - 150 A @ 1.0 A Resolution	
	<i>Reverse Current Slope (di/dt)</i>	25 - 600 A/ μ s in 1.0A/ns Steps	
MIL STD 750 Method 3473	<i>Reverse Voltage (VR)</i>	20 - 600 volts	
	<i>Reverse Current (IRM)</i>	150 A (max)	
	<i>Duty Cycle on DUT</i>	Less than 1.0%	
	<i>Reverse Recovery Time Range (Trr)</i>	10 ns - 2.0 μ s	
	<i>Supply Voltage (V_{DD})</i>	20V to 600V in 2.5V Steps	
	<i>Stored Charge Range (Qrr)</i>	1 nC - 100 μ C	
	ITC 5723B Gate Charge Specifications	<i>Gate Drive</i>	<i>Low</i>
<i>Medium</i>			2.0 - 20.0 mA @ 0.1 mA Resolution
<i>High</i>			20.0 - 200.0 mA @ 1.0 mA Resolution
<i>Gate Voltage Range</i>		\pm 20 volts @ \pm 0.1 V Resolution	
<i>Drain Current (Solid-State Load)</i>		1 - 25 A @ 0.1 A Resolution	
		25 - 150 A @ 0.5 A Resolution	
<i>Drain Current (Inductive Load)</i>		1 - 25.5 A @ 0.1 A Resolution	
		26 - 100 A @ 0.5 A Resolution	
<i>Drain Voltage</i>	0 - 60 V @ +0.25 V Resolution		
	20 - 600 V @ +2.5 V Resolution		
AC Input Requirements	<i>115 Vac 50/60 Hz Single-Phase</i>	240 Vac is optional	
	<i>Maximum Current: 115 Vac = 12A</i>	Input AC Breaker = 20A Service = 30 A	

Note: Specifications subject to change without notice.

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spec sheet